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L26	121 and 13	85	L26
L25	L22 and 17	9	L25
L24	L22 and 16	258	L24
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L22	L21 and 12	1030	L22
L21	(switch\$5 or select\$3 or mux or multiplex\$4) near45 bypass\$3	52752	L21
L20	112 and 17	0	L20
L19	112 and 16	6	L19

<u>L18</u>	l12 and l5	7	<u>L18</u>
<u>L17</u>	L14 and l7	5	<u>L17</u>
<u>L16</u>	L14 and l6	37	<u>L16</u>
<u>L15</u>	L14 and l5	56	<u>L15</u>
<u>L14</u>	L11 and l2	271	<u>L14</u>
<u>L13</u>	L11 and l3	21	<u>L13</u>
<u>L12</u>	L11 and l4	21	<u>L12</u>
<u>L11</u>	L10 near25 (switch\$5 or select\$3 or mux or multiplex\$4)	7753	<u>L11</u>
<u>L10</u>	(by near1 pass\$5 or feed\$3 near3 back or feedback or feedback) near10 (alu\$1 or arithmetic\$3 or add\$4 or multipl\$4 or mul or subtract\$3 or mpy)	49549	<u>L10</u>
<u>L9</u>	(switch\$5 or select\$3 or mux or multiplex\$4) and l4	55	<u>L9</u>
<u>L8</u>	(switch\$5 or select\$3 or mux or multiplex\$4) and l3	1427	<u>L8</u>
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<u>L7</u>	(710/305-317)[CCLS]	5852	<u>L7</u>
<u>L6</u>	(712/201-225,227,245-248)[CCLS]	7261	<u>L6</u>
<u>L5</u>	(712/2-300)[CCLS]	13853	<u>L5</u>
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<u>L4</u>	l1 and L3	55	<u>L4</u>
<u>L3</u>	(fetch\$5 or prefetch\$5 or dispatch\$5 or sequencer\$1) near15 (cycle\$1 or clock\$1) near18 (simultaneous\$5 or parallel\$5 or concurrent\$3)	1629	<u>L3</u>
<u>L2</u>	(fetch\$5 or prefetch\$5 or dispatch\$5 or sequencer\$1) near15 (cycle\$1 or clock\$1)	23461	<u>L2</u>
<u>L1</u>	(by near1 pass\$5 or feed\$3 near3 back or feedback or feedback) near10 (arithmetic\$3 or add\$4 or multipl\$4 or mul or subtract\$3 or mpy)	49346	<u>L1</u>

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